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attorney docket number NVIDP007, "Method, Apparatus and Article of Manufacture for Transform, Lighting and Rasterization on a Single Semiconductor Platform" which was filed on December 06, 1999 under serial number 09/454,516, and attorney docket number NVIDP008, "Method, Apparatus and Article of Manufacture for a Vertex Attribute Buffer in a Graphics Processor" which was filed on December 06, 1999 under serial number 09/454,525, and attorney docket number NVIDP009, "Method and Apparatus for a Lighting Module in a Graphics Processor" which was filed on December 06, 1999 under serial number 09/454,524, and attorney docket number NVIDP011, and "Method, Apparatus and Article of Manufacture for a Sequencer in a Transform/Lighting Module Capable of Processing Multiple Independent Execution Threads" which was filed on December 06, 1999 under serial number 09/456,104, and attorney docket number NVIDP012 which were filed concurrently herewith, and which are all incorporated herein by reference in their entirety.--

On page 7, please delete lines 8-11, and insert therefore:

--Figures 1 and 1A illustrate the prior art;--

IN THE CLAIMS

W. E.

Please cancel claims 23-61.

REMARKS

During a review of the above-identified patent application it was noted that a couple of minor inconsistencies existed between the Figures and the written specification. Accordingly, the present document amends this patent application in order to render it self-consistent. No new matter has been added.

Respectfully submitted,  
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Attorney Docket NVIDP010A